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CMOS Analog Circuit Design © P.E. Allen - 2016 Switch Model ¶ An ideal switch is a short-circuit when ON and an open-circuit when OFF. V C = controlling terminal for the switch (V C high switch ON, V C low switch OFF) ¶ Actual switch: r on = resistance of the switch when ON r off = resistance of the switch when OFF V OS

~~LECTURE 14 THE MOS SWITCH AND MOS DIODE~~

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Allen and Holberg - CMOS Analog Circuit Design Page I.2:1 I.1 - INTRODUCTION GLOBAL OBJECTIVES ¶ Teach the analysis, modeling, simulation, and design of analog circuits implemented in CMOS technology. ¶ Emphasis will be on the design methodology and a hierarchical approach to the subject.

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Welcome to AICDesign. This site is dedicated to providing resources for students and professionals in the area of analog integrated circuit design using CMOS and BiCMOS technologies. The resources are designed to help understand the principles, concepts, and techniques of analog integrated circuit design.

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J.F. Duque-Carrillo, ¶Control of the common-mode component in CMOS continuous-time fully differential signal processing¶. Analog Integrated Circuits and Signal Processing 4, 131-140, Kluwer Academic Publishers, 1993.

~~1- Analog Integrated Circuit Design—SUN's LAB~~

P.E. Allen, D.R. Holberg, CMOS Analog Circuit Design, Holt, Reinhart, and Winston, Inc., 1987. This is one of very few books dedicated entirely to MOS analog circuits. We will use some of the material in chapter 8 on the topic of CMOS op-amp design. D.A. Johns, K. Martin, Analog Integrated Circuit Design, J. Wiley & Sons, 1997. This is also a ...

~~EE140/EE240A, Fall 2019~~

CMOS Analog Circuit Design. Phillip E. Allen, Douglas R. Holberg. Holt, Rinehart & Winston, 1987 - Technology & Engineering - 701 pages. 2 Reviews. This text presents the principles and techniques...

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