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Toward a Culture of Computational Reproducibility [Simulation And Debug Of Mixed](#) Simulation and Debug of Mixed Signal Virtual Platforms for Hardware-Software co-development. Vincent Motel, Cadence Design Systems, Inc. (vmotel@cadence.com) Alexandre Roybier, Cadence Design Systems, Inc. (aroybier@cadence.com) Serge Imbert, Cadence Design Systems, Inc. (sergeim@cadence.com) Abstract Virtual platforms are often used for high level architecture exploration and hardware-software interactions verification.

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Simulink \u00a9 supports debugging with Simulation Stepper, which lets you step back and forth through your simulation. Use the Simulation Stepper to view data and inspect how and when the system changes states. For information, see How Simulation Stepper Helps With Model Analysis.. Simulink also includes the Simulink Debugger, which, like the Simulation Stepper, also enables you to start, stop ...

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supports debugging with Simulation Stepper, which lets you step back and forth through your simulation. Use the Simulation Stepper to view data and inspect how and when the system changes states. For information, see [How Simulation Stepper Helps With Model Analysis](#)..

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Simulink also supports debugging with the Simulation Stepper, which lets you step back and forth through your simulation, viewing data and inspecting how and when the system changes states. For information, see [How Simulation Stepper Helps With Model Analysis](#) .

~~Simulink Debugger - MATLAB & Simulink~~

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~~SimVision Debug—Cadence~~

Synopsys has announced that Almotive has adopted Synopsys VCS simulation and Verdi debug, part of the Verification Continuum Platform, to help verify its innovative aiWare hardware IP for Neural Network (NN) acceleration for automated driving applications.

~~Synopsys VCS simulation to verify automated technologies~~

Simulate mixed-signal designs with the Spectre AMS Designer/Xcelium mixed-signal simulators using command-line control. Learn to run simulations and debug them using the SimVision Interactive debugger. Simulate with a single-step xrun command, learn to use its associated control files.

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Mixed HDL Simulation. ModelSim combines simulation performance and capacity with the code coverage and debugging capabilities required to simulate multiple blocks and systems and attain ASIC gate-level sign-off. Comprehensive support of Verilog, SystemVerilog for Design, VHDL, and SystemC provide a solid foundation for single and multi-language design verification environments.

~~ModelSim ASIC and FPGA Design—Mentor Graphics~~

The initialization, running, debugging and maintenance of these systems are extremely difficult tasks that, if not carefully performed, can use up the resource savings that mixed-reality simulations are designed to achieve. These issues are addressed through a set of computer applications known as the Mixed Reality Toolbox or, MRT.

~~Mixed-Reality Training Simulation | Virtual Reality ...~~

Multi-Core simulation which supports all design languages and constructs and either automatically or manually partitions the design to run in parallel while maintaining a single database for debug and coverage.

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